

Electronic Acknowledgement Receipt

EFS ID:	3198011
Application Number:	09919372
International Application Number:	
Confirmation Number:	2525
Title of Invention:	Clock distribution circuits and methods of operating same that use multiple clock circuits connected by phase detector circuits to generate and synchronize local clock signals
First Named Inventor/Applicant Name:	Vadim Gutnik
Customer Number:	20792
Filer:	David Moore/Kirsten Carlos
Filer Authorized By:	David Moore
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		Amendment.pdf	802318 e818b7a432e55272db6ae710e0a3ce845c64ebe5	yes	12

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Amendment - After Non-Final Rejection	1	1
	Claims	2	8
	Applicant Arguments/Remarks Made in an Amendment	9	12
Warnings:			
Information:			
Total Files Size (in bytes):		802318	
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